# **EL640.480-AM8 Series TFEL VGA Displays**



# EL640.480-AM8 Series (10.4")

# **Operation Manual**

**Beneq Oy** Olarinluoma 9 FI-02200 Espoo Finland

Date: November 1, 2019

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# 1 AM8 series 10.4" VGA flat panel displays

The AM8 series of 10.4" diagonal VGA products offers designers an affordable and easily implemented path to incorporate flat panel solutions to meet display requirements.

Utilizing Lumineq Displays' proprietary ICEBrite™ technology (Integral Contrast Enhancement), these displays excel in ambient light environments ranging from dark rooms to nearly sunlight. This proprietary technology achieves very high contrast and exceptionally clear images. Up to five distinct gray levels are enabled by frame rate modulation algorithms generated by the controller chips.

Through simple design and advanced manufacturing technologies, Lumineq's new VGA displays bring the visual performance and image quality of thin film electroluminescence (TFEL), long life, and ruggedness into cost-sensitive applications.

#### 1.1 Features and benefits

- Excellent viewing characteristics
- High brightness and contrast
- Wide viewing angle
- Long life
- Extreme ruggedness
- Designed for low EMI
- Fast response time
- Low power

#### **Options** 1.2

Anti-glare and conformal coating options are available on this display.

The anti-glare option adds an anti-glare film to the front of the display to reduce specular reflections.

The conformal coating option adds a protective layer to the display for applications in which the display may be subjected to high humidity, dust, or salt mist.

# 2 Installation and handling

The mechanical package consists of the display panel and electronic circuit board bonded together, plus a protective cover carrying the display mounting ears. The display is made of glass material and must be handled with care.

CAUTION: The display uses CMOS and power MOSFET devices. These components are electrostatic sensitive. Unpack, assemble, and examine this assembly in a static-controlled area only. When shipping, use packing materials designed for protection of electrostatic-sensitive components.

Do not drop, bend, or flex the display. Do not allow objects to strike the surface of the display.

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# 2.1 Mounting TFEL displays

The EL640.480-AM8 displays were designed to facilitate the mounting of optical treatments and touch screens on the viewing side of the display. To this end, the glass extends in front of the mounting bezel by approximately 0.5 mm (0.02 in.). It is imperative that any mounting scheme apply uniform pressure at all times across the glass. Pressure applied to the corners or edge of the glass is likely to cause breakage.

Mounting should be done using the mounting ears only. Use either metric M3 screws torqued to 350 Ncm maximum, or unified #4 screws torqued to 4.7 in/lb maximum.

**CAUTION:** Properly mounted, this display can withstand high shock loads and severe vibration in aggressive environments. However, the glass panel used in this display will break when subjected to bending stresses, high impact, or excessive loads.

To prevent injury in the event of glass breakage, a protective overlay should be used on the viewer side of the display.

**WARNING:** These products generate voltages capable of causing personal injury (high voltage up to 200 V<sub>AC</sub>). Do not touch the display electronics during operation.

# 2.2 Cable length

The maximum recommended cable length is 600 mm (24 in.). Longer cables may cause data transfer problems between the data transmitted and the display input connector. Excessive cable lengths can pick up and source unwanted EMI.

#### 2.3 Cleaning

As with any glass or coated surface, care should be taken to minimize scratching. Clean the display glass with mild, water-based detergents only. Apply the cleaner sparingly to a soft cloth, and then wipe the display. Disposable cleaning cloths are recommended to minimize the risk of inadvertently scratching the display with particles embedded in a re-used cloth. Care should be taken when cleaning displays with anti-glare and anti-reflective films.

The TAB (tape-automated bonding) leads between the electronics and the display glass are very sensitive to handling. When cleaning the edges of the display glass, special care should be taken not to damage the leads.

# 2.4 Avoiding burn-in

As with other light emitting displays, displaying fixed patterns on the screen can cause burnin, where luminance variations can be noticed. Use a screen saver or image inversion to avoid causing burn-in on the display.



# 3 Specifications

The TFEL panel is a matrix structure with column and row electrodes arranged in an X-Y formation. Light is emitted when an AC voltage of sufficient amplitude is applied at a row-column intersection. The display operation is based on the symmetric, line at a time data addressing scheme. Operating voltages required are provided by a DC/DC converter.

Unless otherwise specified, performance characteristics are guaranteed when measured at  $25\ ^{\circ}\text{C}$ .

#### 3.1 Power

The supply voltages required for the displays are shown in Table 1. All internal high voltages are generated from display supply voltage ( $V_H$ ).

Table 1. DC input voltage requirements

	AM8	IN/ET	Notes	
Power in	+12 VDC	+24 VDC	Selected input voltage, J3	
V <sub>H</sub>	+12 VDC ±10%	+24 VDC ±10%	absolute max. 15 VDC and 30 VDC respectively	
I <sub>H</sub> , max	2.0 A	1.0 A	@ V <sub>H</sub> = +12 VDC	
P <sub>typ</sub> , 120 Hz	11 W	11 W	3840 'E' characters	
P <sub>max</sub> , 120 Hz	24 W	24 W	50/50 2x2 checkerboard	

**CAUTION:** Absolute maximum ratings are those values beyond which damage to the device may occur.



**Table 2. Video input requirements** 

Description	Min	Max	Notes
Video logic high voltage	3.7 V	5.0 V	All input thresholds are CMOS
Video logic low voltage	0 V	0.9 V	Video lines have 100 $\Omega$ series resistors
Video logic input current	-10 µA	+10 µA	

There is no overcurrent protection on either the  $V_H$  or  $V_L$  inputs to protect against catastrophic faults. Beneq recommends the use of a series fuse on the supply input line. A general guideline is to rate the fuse at 1.8 to 2 times the display maximum current rating.

# 3.1.1 Input voltage selection

The feature a jumper that allows the user to choose between +12 VDC and +24 VDC supply voltages. The factory default is +12 VDC.

The display voltage selections are marked on the printed circuit board.

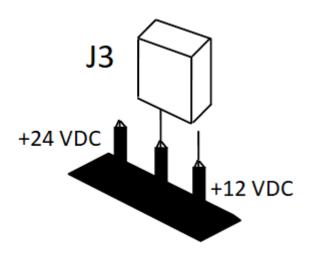


Figure 1. AM8 power input selection

**CAUTION:** Providing +24 VDC supply to the display when the jumper is in the +12 VDC supply position will damage the display.

The display will not function if the jumper is removed completely. Beneq recommends the use of a non-conductive adhesive to secure the jumper in the desired position in environments where shock or vibration might loosen the jumper.

The minimum and maximum specifications in the manual should be met, without exception, to ensure the long-term reliability of the display. Beneq does not recommend operation of the display outside these specifications.

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#### 3.2 Connectors

# 3.2.1 Data and power connector

The locking connector Samtec EHT-110-01-S-D or equivalent is standard on the AM8 ET displays. The mating connector for both the AM8 ET and AM8 IN displays is in the Samtec TCSD family of cable strips. The proper connector, user-specified cable length, and connector configuration are supplied as a single unit. Consult your Samtec representative for the cable/connector options.

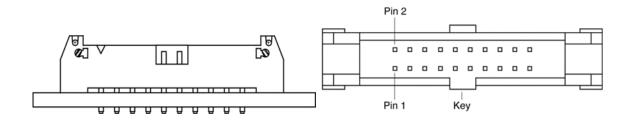


Figure 1. AM8 IN data/power connector

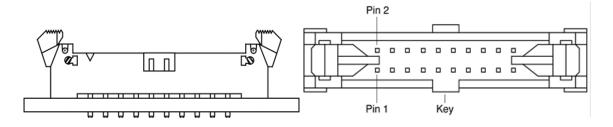


Figure 3. AM8 ET data/power connector

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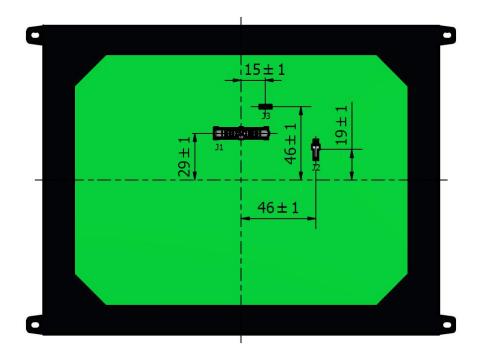


Figure 4. AM8 connectors and jumper

# 3.2.2 AM8 data and power connector pin outs

Table 3. J1 connector pinouts

Pin	Signal	Description	Pin	Signal	Description
1	UD1	Video data (upper)	2	UD0	Video data (upper)
3	UD3	Video data (upper)	4	UD2	Video data (upper)
5	LD1	Video data (lower)	6	LD0	Video data (lower)
7	LD3	Video data (lower)	8	LD2	Video data (lower)
9	CP2	Pixel clock	10	GND	Ground
11	CP1	Line pulse	12	GND	Ground
13	S	Frame pulse	14	GND	Ground
15	GND	Ground	16	GND	Ground
17	NC	Not connected	18	NC	Not connected
19	V <sub>H</sub>	+12/24 V Display supply	20	V <sub>H</sub>	+12/24 V Display supply

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# 3.2.3 Dimming connector J2

The J2 dimming connector is a Molex 2-pin, right angle locking male, part number 22-12-2024. The recommended mating connector is a Molex 22-01-3027 (housing) and 08-52-0123 (crimps, 2 per housing).

Additionally, the crimp housing will need crimp terminals to connect the wires, Molex 2759 or 6459 can be used. Molex crimp terminal 41527 is recommended for applications with high vibration requirements.

For a discussion of the dimming feature, refer to the Dimming section below, under Optical Performance.

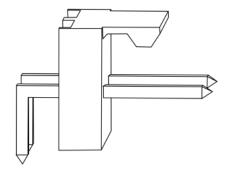


Figure 5. Dimming connector

# 3.3 Interfacing

This dual scan LCD-type video interface provides a low cost, flexible method for controlling display brightness and power consumption.

#### 3.3.1 Video input signals

The end of the top line of a frame is marked by S, scan start up signal as shown in Figure . The end of each row of data is marked by CP1 as shown in Figure and Figure .

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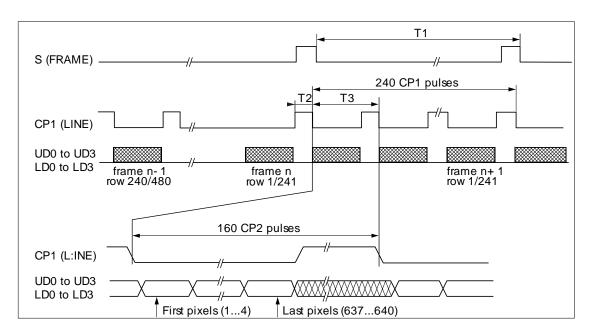


Figure 6. Video input timing diagram

**Table 4. Video input descriptions** 

Symbol	Name	Min.	Max.	Units
T1 Frame time		8.3		ms
	Frame frequency		120	Hz
T2 CP1 High time		60		ns
T3	CP1 Period	34.5		μs

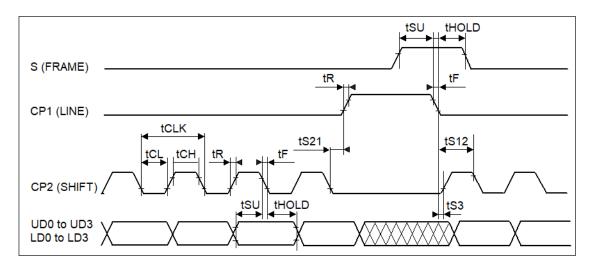


Figure 7. Setup and hold timing diagram

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Table 5. Setup and hold timing

Symbol	Symbol Name		Max.	Units
tS21	tS21 CP1 allowance from CP2			ns
tS12	CP2 allowance from CP1	200		ns
tS3	CP1 allowance to CP2	50		ns
tSU	Setup time	50		ns
tHOLD Hold time		40		ns
tR Rise time			30	ns
tF Fall time			30	ns
tCLK CP2 clock cycle		154		ns
tCL	CP2 clock low time	60		ns
tCH	tCH CP2 clock high time			ns

Input signals UD0 through UD3 contain the video data for the upper screen and signals LD0 through LD3 contain the data for the lower screen. For example, four pixels (UD3:1,1-UD0:1,1) are sent to row 1 at the same time as four pixels (LD0:1,1-LD3:1,1) are sent to row 241. These results in eight pixels sent on each cycle of video clock CP2. Pixel information is supplied from left to right and from top to bottom. Video data for one row is latched on the fall of CP1 (6).

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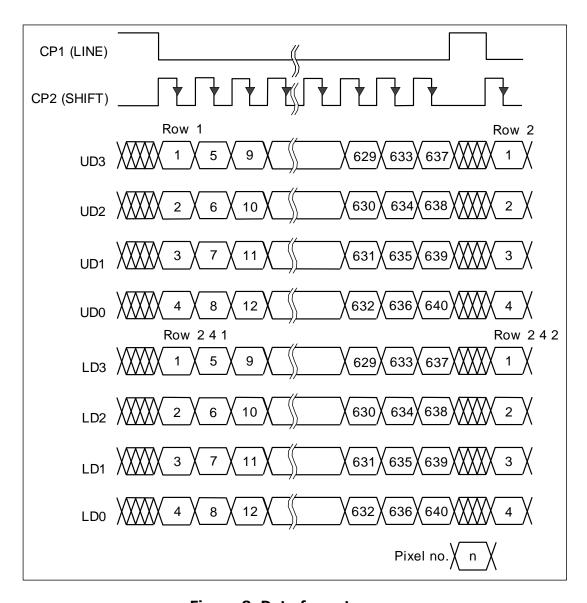


Figure 8. Data format

#### 3.4 Self-test mode

The display self-test mode is entered by applying power to the display with the static S (Frame) signal. The self-test contains 1x2 checkerboard, 1x2 inverted checkerboard, full screen and black screen patterns. The self-test run in a loop and each pattern is displayed in 4 seconds.

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# 3.5 Optical performance

#### **Table 6. Optical characteristics**

Luminance	L <sub>on</sub> (area	l), min	L <sub>on</sub> (areal), typ	L <sub>off</sub> (areal), max			
AM8 IN/ET	35 cd/m <sup>2</sup>		49 cd/m <sup>2</sup>	0.3 cd/m <sup>2</sup>			
Minimum and Typical: Center, 120 Hz frame rate							
Maximum: Center and four corners, 120 Hz frame rate							
Non-uniformity							
AM8 IN/ET	26%	Maximu	ım difference two of fiv	e points, using the			
		formula	ı: BNU%=[1- (min_lur	n/max_lum)] x 100%			
Luminance variation (Temperature)							
Maximum	±20%	Across	operating temperature	range			
Luminance varia	ation (Time	e)					
Maximum							
AM8 IN/ET	10%	10,000 hours at 25 °C ambient					
Viewing angle	-1	I.					
Minimum 179°							
Contrast ratio	Minimum	<u> </u>	Typical				
AM8 IN/ET 35:1			70:1				
Minimum: @ 500 lux ambient, @ 120 Hz frame rate							

#### **3.5.1 Dimming**

There are two standard methods for dimming the AM8 displays. Frame rate dimming allows the brightness of the display to be lowered proportionally by reducing the frame rate. Analog dimming allows the brightness to be reduced with an external, user supplied potentiometer or electronic circuitry. The brightness range for analog dimming is from 100% to nearly 5% of the full brightness.

Frame rate dimming is performed within the application by lowering the frame rate of the video input signals. Because brightness is proportional to refresh rate, the display can be dimmed by adding pauses between every horizontal period.

The lowest possible frame rate (brightness) is dependent on the perceived flicker of the displayed image but will most likely be around 60 Hz.

The analog dimming feature utilizes a 50k ohm logarithmic external potentiometer, allowing manual control of the display's brightness.

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# 3.5.2 Grayscale

Using the display's higher frame rate frequency, combined with flat panel controllers, it is possible to generate a wider range of usable grayscales through frame rate control and dithering.

Frame Rate Control generates true grayscales by turning pixels on and off over several frames of time. The user's eye sees each individual pixel as grey. The potential problems with this method are brightness-profile and shadowing, especially when used to shade larger areas.

Dithering generates grayscales by patterning the area to be shaded. When the area is viewed from a distance, it appears to the user's eye as grey. When viewed up close, the different brightness of the individual pixels can be observed.

Many of the grayscales that can be generated may flicker or swim. Through careful selection however, several grayscale patterns are available for most applications, particularly when the display is driven at its maximum frame rate frequency.

# 3.6 Reliability

The MTBF (mean time between failures) is a minimum of 50,000 hours at 25 °C ambient, except for performance characteristics with an otherwise specified life expectancy.

# 3.7 Safety and EMI performance

The AM displays will not inhibit the end product from obtaining these certifications:

Safety: UL544, IEC950

EMI: FCC Part 15/J, Class B

EN55022 Level B



# 3.8 Environmental

#### **Table 7. Environmental characteristics**

Temperature	AM8 IN	AM8 ET					
Operating	-25 to +65 °C	-60 to +85 °C					
Non-operating	-40 to +75 °C	-60 to +110 °C					
Operating survival	-40 to +70 °C -60 to +105 °C						
Humidity							
Non-condensing	93% RH max +40 °C per IEC 6	0068-2-78.					
Altitude	Altitude						
Operating 0 to 18 km (58k ft)							
Non-operating	0 to 18 km (58k ft) per IEC 600	068-2-13					
Vibration							
Random	0.02 g <sup>2</sup> /Hz						
	ASD level, 20-500 Hz per IEC 60068-2-64, test Fdb						
Shock							
Half sine wave 100 g, 6 ms 18 (3 each, 6 surfaces) per IEC 60068-2-27, test							

# 3.9 Mechanical characteristics

# Table 8. Dimensions and weight

Display Size	266 x 192 (W x H)
Display Depth	19
Active Area	211.1 x 158.3 (W x H)
Fill Factor	57%
Pixel Pitch	0.33 x 0.33
Pixel Size	0.25 x 0.25
Weight (typical)	650 g

All dimensions are in millimetres, unless otherwise noted.

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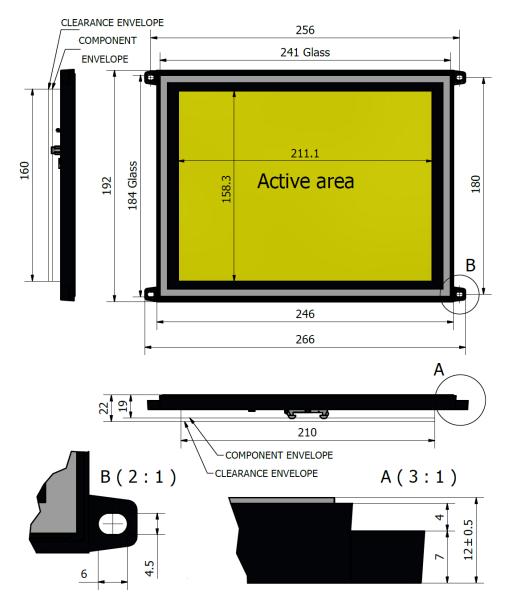
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# 3.10 Component envelope

Figure 9 illustrates the mechanical dimensions and the distance components extend behind the display. Tall components do not necessarily fill this area. Beneq reserves the right to relocate components within the constraints of the component's envelope without prior customer notification. For this reason, Beneq advises users to design enclosure components to be outside the component envelope.



Dimensions are in millimeters.

Tolerances unless otherwise stated  $\pm$  0.2 mm.

Figure 9. AM8 series dimensions

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# 4 Description of warranty

Seller warrants that the Goods will conform to published specifications and be free from defects in material during warranty time from delivery. To the extent that goods incorporate third party-owned software, seller shall pass on seller's licensor's warranty to buyer subject to the terms and conditions of seller's license.

Warranty repairs shall be warranted for the remainder of the original warranty period. Buyer shall report defect claims in writing to seller immediately upon discovery, and in any event, within the warranty period. Buyer must return goods to seller within 30 days of seller's receipt of a warranty claim notice and only after receiving seller's return goods authorization. Seller shall, at its sole option, repair or replace the goods.

If goods were repaired, altered or modified by persons other than seller, this warranty is void. Conditions resulting from normal wear and tear and buyer's failure to properly store, install, operate, handle or maintain the goods are not within this warranty. Repair or replacement of goods is seller's sole obligation and buyer's exclusive remedy for all claims of defects. If that remedy is adjudicated insufficient, Seller shall refund buyer's paid price for the goods and have no other liability to buyer.

All warranty repairs must be performed at seller's authorized service centre using parts approved by seller. Buyer shall pay costs of sending goods to seller on a warranty claim and seller shall pay costs of returning goods to buyer. The turnaround time on repairs will usually be 30 working days or less. Seller accepts no added liability for additional days for repair or replacement.

If seller offers technical support relating to the goods, such support shall neither modify the warranty nor create an obligation of seller. Buyer is not relying on seller's skill or judgment to select goods for buyer's purposes. Seller's software, if included with goods, is sold as is, and this warranty is inapplicable to such software.

SELLER DISCLAIMS ALL OTHER WARRANTIES, EXPRESS OR IMPLIED, INCLUDING BUT NOT LIMITED TO, IMPLIED WARRANTIES OF MERCHANTABILITY AND FITNESS FOR A PARTICULAR PURPOSE.

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# 5 Ordering information

Product	Part number	Description
EL640.480-AM8 IN	996-0268-27LF	10.4" diagonal, 640x480 VGA display.
		-25 to 65 °C operating temperature,
		analog dimming, low profile, optional
		24 V supply
EL640.480-AM8 ET	996-0268-16LF	10.4" (mm) diagonal, 640x480 VGA
		display.
		-60 to 85 °C operating temperature,
		analog dimming, low profile, optional
		24 V supply, locking data and power
		connector.
EL640.480-AM8 ET CC	996-0268-20LF	EL640.480-AM8 ET with conformal
		coating

Design and specifications are subject to change without notice.

Beneq continues to provide optional, and in many cases custom, features to address the specific customer requirements. Consult Beneq Sales for pricing, lead time and minimum quantity requirements.



# 6 Support and service

Beneq Oy is a Finnish company based in Espoo, Finland, with a world-wide sales distribution network. Full application engineering support and service are available to make the integration of Lumineq displays as simple and quick as possible for our customers.

**RMA Procedure:** For a Returned Material Authorization number, please contact Beneq Oy by email (rma.lumineq@beneq.com) with the model number(s), serial number(s) and brief description of the problem. When returning goods for repair, please include a brief description of the problem, and mark the outside of the shipping container with the RMA number.

# 7 ROHS III

Lumineq EL640.480-AM8 Series displays are RoHS3 (Restrictions of Hazardous Substances in Electronic/Electrical Equipment) compliant and meet the requirements defined under European Union Directive (2015/863), that restrict the use of various hazardous substances in electronic equipment

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